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Whole RTL Design Code Is Written In Verilog For Synthesis And Its Verification Code Is Written In System Verilog, IEEE (2005). Keywords - Serial Peripheral Interface (SPI), System Verilog, System- On-Chip (SoC), Intellectual Property (IP). Jul 1th, 2024

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MO) To The Digestion Buffer And Culture Media. Neurons Were Cultured For 24–48 Hours Before Use. Ca21 Imaging Techniques Have Also Been Described.39 Briefly, Cultured Cells On Coverslips Were Loaded With The fluorescent Dye Fura-2 For 1 Hour At 37°C In Hank's Balanced Salt Solution. The Coverslips Were Mounted May 4th, 2024

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