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EECE488 Analog CMOS Integrated Circuit Design Assignment 4 Due: Tuesday November 26th, 2013 At 8:00am 1. Consider The Following Circuit. (This Circuit Is Sometimes Referred To As A Self-biased Current Source). $R = 10k\Omega$, $V_{DD} = 3V$, $V_{TH(NMOS)} = 0.7V$, $V_{TH(PMOS)} = 1.2V$, $\beta_{NMOS} = 100\mu A/V^2$, $\beta_{PMOS} = 50\mu A/V^2$, $V_{GS} = 0V$, $V_{DS} = 0V$, $I_{DQ} = 0A$. Assume All Transistors Are Operating In Saturation Region And $\lambda = 0$. Mar 2th, 2024

EE 338L: Analog Integrated Circuit Design Fall 2012

Output Stages; Analog Filters. CAD Tools For Circuit Analysis And Design. Textbook Behzad Razavi, Fundamental Of Microelectronics, Wiley, 2008. Other Helpful References: Paul Gray Et Al., Analysis And Design Of Analog Integrated Circuits, Wiley, 2009. Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw-Hill, 2000. Mar 3th, 2024

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