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Dr. Tao Li 7 •Operand Field –Can Be: Names Of Registers, Numeric/symbolic Constants, Labels, Algebraic Expressions To Be Evaluated By Assembler –The Following 2 Instructions Load The A, B Registers From Memory Location DATA1 And DATA1+1 •Comment Field –Ignored During Assembly –May Have Source Code Lines That Are Only Comments Or Blank Jul 3th, 2024

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WRITE; Indicates The Data On The Data Bus Is To Be Written Into The Selected Memory Or 1/0 Location. Data Is Set Up At The Trailing Edge Of WR. 3 Stated During Hold And Halt Modes. READY (Input) If Ready Is High During A Read Or Write Cycle, It Indicates That The Memory Or Peripheral Is Ready To Send Or Receive Data. Mar 1th, 2024

Week 2 8051 Assembly Language Programming Chapter 2

2 Outline 2.1 Inside The 8051 2.2 Introduction To 8051 Assembly Programming 2.3 Assembling And Running An 8051 Program 2.4 The Program Counter And ROM Space In The 8051 2.5 8051 Data Types And Directives 2.6 8051 Flag Bits And The PSW Register 2.7 8051 Register Banks And Stack Feb 4th, 2024

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