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SM 3 EECE 488 – Set 1: Introduction And Background References • Main Reference: Lecture Notes • Recommended Textbook: Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw- Hill, 2001 • Some Other Useful References: 1th, 2024

EECE488 Analog CMOS Integrated Circuit Design

EECE488 Analog CMOS Integrated Circuit Design Assignment 4 Due: Tuesday November 26th, 2013 At 8:00am 1. Consider The Following Circuit. (This Circuit Is Sometimes Referred To As A Self-biased Current Source). $R = V_{DD} / GND$ M_0 M_2 M_1 M_3 GND I_{Out} Assume All Transistors Are Operating In Saturation Region And $V_{DD} = 3V$, $V_{TH}(NMOS) = 0 \dots$ 3th, 2024

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Analog CMOS Integrated Circuit Design ... R. Jacob Baker, CMOS Circuit Design, Layout, and Simulation, 3rd ... Circuit Design, E.g., V_{TH} May Be Zero and the Device May Be On for Any Positive Gate Voltage. • Typically threshold voltage is adjusted by ion implantation into the 1th, 2024

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