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To Face. In A Mixed-signal System-on-Chip (SoC), I.e., When Analog And Digital Circuits Are Integrated On The Same Silicon Chip, Performance Limitations Come Mainly From The Analog Section Which Interfaces The Digital Processing Core With The External World. In Such ICs, The Digital Switching Activity May Affect The Analog Section. V DDA,ext I ... 1th, 2024

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I Would Also Thank My Grandparents , V Padmavathi And V.A.S.N Rao, My Mother, Satyavada Kamala, My Father, Satyavada Subbarao And My Sister, Satyavada Srujana For Their Love And For Always Supporting Me And Encouraging Me Through Out My Journey. I Would Like To Thank Glory Swathi Tirunagari F 2th, 2024

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Sin Encoder - Mixed-signal And Digital Signal Processing ICs

Digital I/O Unit Encoder Interface Event Capture Unit Watchdog Timer PM ROM 2k X 24 DM RAM 1k X 16 PM RAM 2k X 24 MEMORY SERIAL PORTS SPORT0 SPORT1 Precision Voltage Reference Power On Reset EXTERNAL ADDRESS BUS EXTERNAL DATA BUS Figure 4: Block Diagram For The A 3th, 2024

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