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6 Cad-edi Flow 1. 5.Import Design .v, .sdc, .lib, .lef – Can Put This In A File.conf And Default.view 2. Power Plan Rings, Stripes, Row-routing (sroute) 3. Placement Place Cells In The Rows 4. Timing Optimization – PreCTS Cad-edi Flow Synthesize Clock Tree Feb 3th, 2024

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A. Identifiers And Keywords In PASCAL B. Unsigned Numbers In PASCAL 4. Consider The Following Program: Main {int X,y,z; Z=x+y;} List Down The Lexemes, Tokens And The Attributes Of The Tokens At The End Of The Lexical Analysis. 5. What Do You Mean By Compilation Process? Explai Apr 1th, 2024

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