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Digital Logic Combinational Logic Design Process

...Step 2 Convert To Equations This Step Is Only Necessary If You Captured The Function Using A Truth Table Instead Of Equations. Create An Equation For Each Output By ORing All The Minterms For That Output. Simplify The Equations If Desired. Step 3

Implement As A Gate-based Circuit For Each O 3th,

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That Performs Arithmetic Operations Such As Addition,

Subtraction, Multiplication And Division With Binary

Numbers Or With Decimal Numbers In A Binary Code.

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