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Other References: VLSI Test Principles And Architectures, L-T Wang, C-W Wu And X. Wen, Morgan Kaufmann, 2006. 2th, 2024

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The Conservative Logic Gates Are Feynman, Toffoli, And Fredkin. The Design Of Two Vectors Testable Sequential Circuits Based On Conservative Logic Gates. All Sequential Circuit Based On Conservative Logic Gates Can Be Tested For Classical Unidirectional Stuck-at Faults 1th, 2024

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By(t)jdt Z 1 1 Ajx(t)j+ Bjy(t)jdt Triangle Inequ 2th, 2024

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Proceedings Of The 57th Annual Meeting Of The Association For Computational Linguistics , Pages 747 763 Florence, Italy, July 28 - August 2, 2019. 4th, 2024

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