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Analog/digital And Digital/analog Converters, CPU, DSP, ASIC, FPGA Advantages:
¾noise Is Easy To Control After Initial Quantization ¾highly Linear (with Limited Dynamic Range) ¾complex Algorithms Fit Into A Single Chip ¾flexibility,
Parameters Can Be Varied In Software 2th, 2024Discrete Time Signal
ProcessingComputer (Figure 1.1(b)). These Represent A Continuous-time, Discrete-

time And Digital Signal Respec-tively. As We Know From The Sampling Theorem, The Continuous-time Signal Can Be Reconstructed From Its Sam-ples Taken With A Sampling Rate At Least Twice The Highest Frequency Component In The Signal 4th, 2024Signals & Systems Discrete-Time Signal ProcessingSignals, Systems And Inference, Global Edition For Upper-level Undergraduate Courses In Deterministic And Stochastic Signals And System Engineering An Integrative Approach To Signals, Systems And Inference Signals, Systems And Inference Is A Comprehensive Text Th 2th, 2024.

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