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Semi-Dynamic And Dynamic Flip-FLOps With Embedded Logic In Troductioii Fabian Mass Sun Microsystems Inc. Palo Alto, CA 94303 USA This Paper Describes A Family Of Semi-dynamic And Dynamic Edge-triggered Flip-flops To Be Used With Static And Dynamic Circuits, Respectively [1][2]. The Flip-flops Provide Both Short 2th, 2024

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W Cross Section L Gland Depth Squeeze E(a) Diametral  
Clearance G-GrooveWidth R Groove Radius Max.  
Eccentricity Nominal Actual Actual % (b) No Back-Up  
Ring(G) One 3th, 2024

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Pranav Dharwadkar Howard Jay Siegel<sup>1</sup> Edwin K. P. Chong Purdue University School Of Electrical And Computer Engineering 1285 Electrical Engineering Building West Lafayette, IN 47907- 1285 USA {dharwadk, HJ, Echong} @ecn.purdue.edu July 2000 Purdue University School Of Electrical And Computer Engineering Technical Report TR-ECE 00-9 1th, 2024

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Fractals In The Sense That Fractals Create Forms That Possess Regions On “the Edge Of Chaos.” Complexity



Is Illustrated Below In Figure 1 And Will Be Discussed Further In Relation To Fractals Later. Figure 1 (created By Priya Roy): Looking At Images A-D, Which Of The Images Is The Most Complex? 3th, 2024

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