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Clock Is Usually Derived From An AES Or S/ PDif Bit
Stream. And Like The Analog To Digital Converter, This
Regeneration Process Can Introduce Jitter Into The
Sample Clock 2th, 2024Analog To Digital (Adc) And
Digital To Analog (Dac) ...Figure 6. Three-bit, Flash
Type Analog To Digital Converter Circuit Analog To
Digital Converter (ADC): When Selecting An Analog To
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Characteristics Must Be Considered. Similar T 1th,
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