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Funding Request: Total Project Cost \$350,000. Funding Request \$170,000. Provided By WBWCD \$180,000 (\$88,400 Cash And \$91,600 In-kind). Project Summary Jan 3th, 2024 LECTURE NOTES ON VLSI DESIGN B.Tech VII Semester (R16) VLSI DESIGN B.Tech VII Semester (R16) Mr.V.R Seshagiri Rao , Associate Professor Dr. V Vijay, Associate Professor Dr. M Manisha, Associate Professor Ms K.S.Indrani, Assistant Professor ELECTRONICS AND COMMUNICATION ENGINEERING INSTITUTE OF AERONAUTICAL ENGINEERING (Autonomous) DUNDIGAL, HYDERABAD - 500043 Jul 2th, 2024.

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