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• Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory •  $T_{Cache} = 1$  Cycle,  $T_{MM} = 100$  Cycles • What Is The AMAT Of The Program From Example 1?  $AMAT = T_{Cache} + MR_{Cache} (t_{MM}) = [1 + 0.375(100)]$  Cycles = 38.5 Cycles Memory Performance Example 2 Apr 2th, 2024

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Memory Hierarchy 19 CS@VT Computer Organization II ©2005-2015 CS:APP & McQuain Caches Cache: A Smaller, Faster Storage Device That Acts As A Staging Area For A Subset Of The Data In A Larger, Slower Device. Fundamental Idea Of A Memory Hierarchy: - For Each K, The Faster, Smaller Device At Level K Serv Apr 2th, 2024

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