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ERRATA PCI Design Handbook-Precast And Prestressed Concrete Third Edition MNL-120-85 P. 3-24 —Fig. 3.5.1 Upper Chart, Bottom Curve Should Be  $K_f/r = 150$ . P. 3-31 —Eq. 3.7.2 The Q 4th, 2024

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## **PHY Interface For The PCI Express\* Architecture PCI Express 3**

PHY Functions Which Must Be Incorporated In A PIPE Compliant PHY, And It Defines A Standard Interface Between Such A PHY And A Media Access Layer (MAC) & Link Layer ASIC. It Is Not The Intent Of This Specification To Define The Internal Arc 1th, 2024

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