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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [May 2th, 2024]

### OpenFOAM: A C++ Library For Complex Physics Simulations

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On The Use Of OpenFOAM To Model Oscillating Wave Surge Converters Schmitt, P., & Elsaesser, B. (2015). On The Use Of OpenFOAM To Model Oscillating Wave Surge Converters. Ocean Engineering, 108, 98-104. DOI: 10.1016/j.oceaneng.2015.07.055 Published In: Ocean Engineering Document Jan 2th, 2024

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