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Directly To The Ground. Therefore, CMOS Circuits Consume Much Less Power Than
Other Types Of Circuits. Figure 6-7b Illustrates How NFET And PFET Can Be Fabri-
cated On The Same Chip. Portions Of The P-type Substrate Are Converted Into N-
type Wells By Donor Implantation And Diffusion. Conta Jun 3th, 2024

MOS Capacitor - Chenming Hu

160 Chapter 5 MOS Capacitor $N = N_{\text{Cexp}}[(E_{\text{C}} - E_{\text{F}})/kT]$ Would Be A Meaninglessly
Small Number Such As $10\text{--}60 \text{ cm}^{-3}$. Therefore, The Position Of E_{F} In SiO_2 Is
Immaterial. The Applied Voltage At The Flat-band Condition, Called V_{fb} , The Flat-
band Voltage, Is The Difference Between The Fermi Levels At The Two Terminals.

(5.1.1) ψg And ψs Apr 1th, 2024

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