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Directly To The Ground. Therefore, CMOS Circuits
Consume Much Less Power Than Other Types Of
Circuits. Figure 6-7b Illustrates How NFET And PFET
Can Be Fabri-cated On The Same Chip. Portions Of The
P-type Substrate Are Converted Into N-type Wells By
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MOS Capacitor - Chenming Hu

160 Chapter 5 MOS Capacitor $n = n_c \exp[(E_c - E_f)/kT]$ Would Be A Meaninglessly Small Number Such
As 10^{-60} cm^{-3} . Therefore, The Position Of E_f In SiO_2
Is Immaterial. The Applied Voltage At The Flat-band
Condition, Called V_{fb} , The Flat-band Voltage, Is The
Difference Between The Fermi Levels At The Two
Terminals. (5.1.1) ψ_g And ψ_s Apr 1th, 2024

Semiconductor Devices, Semiconductor Device Fundamentals ...

Semiconductor Device Fundamentals, R.F. Pierret,
Addison-Wesley, 1996. Solid State Electronic Devices,

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