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Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ 4th, 2024Lecture 8: A Design Example - An Electromagnet6 Specifications (Continued) Magnetic Material Microsil B Sat = 1.4 T,  $\mu$ = 15000  $\mu$ 0 ,  $\rho$  M = 7064 Kg/m3 Superperm 80 B Sat = 0.7 T,  $\mu$ = 6000  $\mu$ 0 ,  $\rho$  2th, 2024.

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