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2018 TRIPLE Triple Crown Eligibility List 6.5 First Name ...

Renard Harrison Murrieta CA Angie Hart Davis CA Mike Hartogs San Diego CA Eric Harvey Chula Vista CA ... Lynne March Dds San Diego CA James Marin San Diego CA

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Speed = At Speed = (1 M/s)(10 S) Speed = 10 M/s

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A Triple 10-Bit High Speed Video DAC CMOS, 240 MHz ...

2Note That The ADV7123 Exhibits High Performance When Operating With An Internal Voltage Reference, V REF. 3DAC To DAC Crosstalk Is Measured By Holding One DAC High While The Other Two Are Making Low To High And High To Low Transitions. 4Clock And Data Feedthrough Is A Function Of The Amo Jan 3th, 2024

CMOS, 330 MHz Triple 10-Bit High Speed Video DAC ...

REGISTER DAC DAC BLANK SYNC R9 TO R0 G9 TO G0 B9 TO B0 PSAVE CLOCK DAC ADV7123 DATA REGISTER DATA REGISTER BLANK AND SYNC LOGIC POWER-DOWN MODE VOLTAGE REFERENCE CIRCUIT IOR IOR IOG IOG IOB VREF RSET VAA GND COMP IOB 00215-001 Figure 1. GENERAL DESCRIPTION The ADV7123 (ADV®) Is A Triple High Mar 3th, 2024

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