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Will Release The Local Bus During T4 Of The Cycle When All The Following Conditions Are Met: 1.Request Occurs On Or Before T2. 2.Current Cycle Is Not The

Low Byte Of A Word (on An Odd Address). 3.Current Cycle Is Not The First Acknowledge Of An Interrupt Acknowledge Sequence. 4.A Locked Instruction Is Not Currently Executing. Feb 1th, 2024

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3. Intel 8086 Microprocessor Architecture, Features, And Signals 63 3.1 Introduction 63 3.2 Architecture Of 8086 63 3.2.1 Execution Unit 63 3.2.2 Bus Interface Unit 66 3.2.3 Minimum And Maximum Mode Operations 67 3.3 Accessing Memory Locations 67 3.4 Pin Details Of 8086 70 3.4.1 Function Of Pins Common To Minimum And Maximum Modes 70 Mar 1th, 2024

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Differences Between 8086 And 8088 Microprocessors Though The Architecture And Instruction Set Of Both 8086 And 8088 Processors Are Same, Still There Are Differences Between Them. Following Is The Table Listing The Differences Between The 2 Micropr Jan 3th,

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8086 (1978) Instruction Is Always “call” So No Need To
Specify It External Device Supplies Interrupt Type, Not
“call” Instruction ... Instruction Set. TIME MARCHES ON.
BEYOND THE 8086 8088 (1979) Identical To 8086
Except F Feb 4th, 2024

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