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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ 3th, 2024

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VAG-COM Table Of Contents Getting Started 3-7 Main Screen 8 Select Control Module 9 Open Controller 10-11 Fault Codes 12-13 Measuring Blocks 14-16 Data Logging 17-18 Single Reading 19 Readiness 20-21 Advanced Identification 22 Login 23-24 7-digit PIN/SKC Dialog 25-26 Basic Settings 27-28 ...File Size: 1MBPage Count: 74 3th, 2024

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IEC 801-2 IEC 801-3 IEC 801-4 USB Interface Interface Type Universal Serial Bus Specification Revision 1.0 Configuration Specification 100 -120 V Model 220-240 V Model Input Voltage Range AC 90 To 132 V AC 198 To 264 V Ra 1th, 2024

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